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Bib Data Sheet

## **CONFIRMATION NO. 8371**

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SERIAL NUMBE 10/769,510	FILING DATE 01/30/2004 RULE	CLASS 716	GROUP ART UNIT 2825		ATTORNEY DOCKET NO. 01-490/1C		
APPLICANTS							
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** CONTINUING DATA **********************************							
** FOREIGN APPLICATIONS ************************************							
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/15/2004							
Foreign Priority claimed  yes  no  Net after			SHEE	TS TO	TAL	INDEPENDENT	
35 USC 119 (a-d) condit met Verified and Acknowledged	fter COUNTRY nitials CA	l l	DRAWING CLA		CLAIMS 3		
ADDRESS 24319 LSI LOGIC CORPORATION 1621 BARBER LANE MS: D-106 MILPITAS, CA							
TITLE System and metho	od for optimizing an integr	ated circuit design					
□ All Fees							
				1.16 Fees ( Filing )			
	No to charge/	ES: Authority has been given in Paper  to charge/credit DEPOSIT ACCOUNT time )				essing Ext. of	
RECEIVED 770	No for followir	o for following:			1.18 Fees ( Issue )		
				Other			

☐ Credit